

<b>Notice of References Cited</b>	Application/Control No. 09/671,973	Applicant(s)/Patent Under Reexamination BLANDY, GEOFFREY OWEN	
	Examiner Charles A Harkness	Art Unit 2183	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,412,105	06-2002	Maslennikov et al.	717/151
	C	US-			
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"The Java Virtual Machine Specification" Sun Microsystems, Inc., copyright 1997, <a href="http://java.sun.com/docs/books/vmspec/html/Instructions2.doc14.html">http://java.sun.com/docs/books/vmspec/html/Instructions2.doc14.html</a> .
	V	Sinclair et al, "ASIC Design for Conditional Nested Loops with Predicate Registers", 08/11/1999, Circuits and Systems, 1999. 42nd Midwest Symposium on, vol. 2, pgs. 874-877.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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